

SATHYABAMA UNIVERSITY

(Established under section 3 of UGC Act, 1956)

Declared as Category 'A' University by MHRD, Govt. of India

Jeppiaar Nagar, Rajiv Gandhi Salai, Chennai – 600 119, Tamilnadu. India.



School of Electrical and Electronics

DATE: 30-04-2016 VENUE: VLSI LAB

TIME: 10.30 a.m. to 12 noon

Minutes of Board of Studies Meeting held on 30th April, 2016

- Dr.E.Logashanmugam, Faculty Head of EEE at the outset extended his cordial welcome to all the members of the Governing Body and in his introductory remarks, he appreciated the members for their active participation in curriculum revision process. The BOS members expressed their highly appreciation and satisfaction about the courses and activities of the Department. The HODs of ECE, EEE, E&I, E&C and ETCE under the Faculty of Electrical and Electronics, Staff members from the faculty attended the meeting to have an effective interaction with the members as and when required.
- Dr.G.Sundari, Prof. & Head Department of Electronics and Communication started
 the meeting by welcoming both the external and the internal numbers to the Board of
 Studies meeting. The guidelines of the syllabus given by the management were
 explained by HOD to all the members of the BOS. The external members also ratified
 the decision to revise the syllabus under the mentioned guidelines.
- Dr. M. Madhavi Latha, Professor, JNTU College of Engineering, Hyderabad accepted the changes suggested by Mr. Vino in 'Satellite Communication'.
- Dr,N.M. Nandhitha presented the changes made in the subject 'Programming in MATLAB'. She highlights the added miscellaneous topics- file handling, import & Export Low Level File I/O Directory management FTP File Operations Time Computations. Dr.Ram Bilas Pachori, Professor, IIT appreciated the effort done by Dr.N.M.Nandhitha.
- Mr. Vino suggested the changes made in 'Digital Logic Circuits' Dr. M. Madhavi
 Latha accepted the changes. Dr. T. Ravi presented the new course introduced to the
 students 'Probability Theory and Random process'.
- Dr.T.Ravi also presented the new course introduced for PG courses are Testing of VLSI Circuits, High Speed VLSI design, VLSI for Wireless Communication, ASIC Design, Design of Semiconductor memories and RF circuits Design for M.E. Embedded system. The Syllabus Revision carried out in PG courses is Embedded System Modelling Using UML.

 Dr.R.Ramadevi presentedthe changes made in the process control lab by adding experiment related to fuzzy Logic and Neural Network Application as per the suggestion given by Dr.Sivakumaran.

 Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Dr.Susitra, Faculty/EEE to present the curriculum revisions before the board.

 She has presented the old and new syllabus for DC machines and transformer (theory and practical) before the board and discussed the valid additions made in the syllabus.

 Dr.V.Sivachidambaranathan put forth the syllabus of the new courses, 'Advanced Power Electronics', 'Electric Vehicles' for the approval of the board. Dr.N Sivakumaran approved the Syllabus for these new courses.

The following internal and external members were present in the meeting.

INTERNAL MEMBERS

SIGNATURE

1. Dr.E.Logashanmugam

2. Dr.G.Sundari

3. Dr.N.M.Nandhitha

4. Dr.V.Sivachidambaranathan

5. Dr.V.Vijaya Baskar

6. Dr.S.Lakshmi

7. Dr.T.Ravi

8. Dr.V.G.Sivakumar

9. Dr. P. Kavipriya

10. Dr.P.Chitra

11. Dr.S.Karthikeyan

12. Ms.K.Srilatha

13. Dr.R.Ramadevi

14. Mrs.K.Sujatha

SIGNATURE

15. Dr. Susitra

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EXTERNAL MEMBERS

Dr. M. Madhavi Latha Professor, JNTU College of Engineering Hyderabad

Dr.Ram Bilas Pachori Professor, IIT Indore

Dr.Sivakumaran, Professor, NIT, Thiruchirapaali M M 30/9/12



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Jeppiaar Nagar, Rajiv Gandhi Salai, Chennai – 600 119, Tamilnadu. India.



School of Electrical and Electronics

DATE 26-11-2016

VENUE: VLSI LAB

TIME: 10.30 a.m. to 12 noon

Minutes of Board of Studies Meeting

- Dr.E.Logashanmugam, Faculty Head of EEE at the outset extended his cordial
 welcome to all the members of the Governing Body and in his introductory remarks,
 he appreciated the members for their active participation in curriculum revision
 process. The BOS members expressed their highly appreciation and satisfaction about
 the courses and activities of the Department.
- Dr.G.Sundari, Prof. & Head Department of Electronics and Communication started the meeting by welcoming both the external and the internal numbers to the Board of Studies meeting (26-11-2016, 10.30 a.m. to 12.00 noon).
- The guidelines of the syllabus given by the management were explained by HOD to all the members of the BOS.
- The external members also ratified the decision to revise the syllabus under the mentioned guidelines.
- Dr S Lakshmi, proposed syllabus revision in 'Spread Spectrum Communication'.
 Dr.M. Madhavi Latha, Professor, JNTU College of Engineering, Hyderabad accepted the changes.
- Dr. M. Madhavi Latha accepted the changes incorporated in the subject' Audio Signal Processing suggested by Dr.P.kavipriya.
- Dr.S.Karthikeyan presented the changes made in 'Analog Communication'. Dr.Ram Bilas Pachori accepted the changes.
- Ms.K.Srilatha presented the changes made in 'Electronic Circuit II'. Dr.Ram Bilas Pachori accepted the changes.
- Dr T Ravi presented the new 'Object Oriented Programming Lab ' for enriching the programming language to students which will be helpful during placement. The two courses introduced are Radar and Navigational Aids' and 'Television Engineering'

- To enrich the knowledge of student with real time industrial environment the five credit course 'Professional Training 1' was introduced to the students was put forth by Dr.G. Sundari.
- Dr.R.Ramadevi suggested few electives for EIE, AsTheory Of Robotics And Automation, Fundamentals of Mechatronics and Instrumentation And Control In Iron And Steel Industries
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Dr.Rameshbabu, Faculty/EEE to present the curriculum revisions before the board.
- He presented the old and new syllabus for AC machines Lab before the board and discussed the valid additions made in the syllabus.

The meeting ended with vote of thanks from the Faculty Head Dr.E.Logashanmugam.

INTERNAL MEMBERS

SIGNATURE

1. Dr.E.Logashanmugam

2. Dr.G.Sundari

3. Dr.N.M.Nandhitha

4. Dr.V.Sivachidambaranathan

5. Dr.V.Vijaya Baskar

6. Dr.S.Lakshmi

7. Dr.T.Ravi

8. Dr.V.G.Sivakumar K. M. Siyh

9. Dr. P. Kavipriya

10. Dr.P.Chitra

11. Dr.S.Karthikeyan

12. Ms.K.Srilatha

13. Dr.R.Ramadevi

14. Mrs.K.Sujatha

15. Dr. Susitra

External members

Dr. M. Madhavi Latha Professor, JNTU College of Engineering Hyderabad

Dr.Ram Bilas Pachori Professor, IIT Indore

Dr. Sivakumaran, Professor, NIT, Thiruchirappalli M/26/11/14

26/1/14