



SATHYABAMA

INSTITUTE OF SCIENCE AND TECHNOLOGY
(DEEMED TO BE UNIVERSITY)

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SCHOOL OF ELECTRICAL AND ELECTRONICS

Minutes of Board of Studies Meeting held on 28thNOVEMBER 2020

(Virtual Meeting conducted on Zoom Platform (Time: 10.30 a.m. to 12 noon))

- Dr.N.M.Nandhitha, Prof. & Dean School of Electrical and Electronics started the meeting by welcoming both the external and the internal numbers to the Board of Studies meeting (28.11.2020, 10.00 a.m. to 12.00 noon)
- Dr.T.Ravi, Head, Dept. of Electronics and Communication Engineering informed the board that core competencies are identified from the feedback obtained from the students, faculty, Alumni and employers.
- Dr.P.Kavipriya proposed to remove the following topics in Electronic Circuits I (3rd Semester) course: 'Approximate Model- Analysis of CE, CC and CB amplifiers using Approximate model equivalent circuits to obtain gain, input impedance and output impedance'. Dr.M.D.Selvaraj, Associate Professor, IIITDM accepted the changes and suggested to include Frequency response of the Differential amplifier in unit V instead of power supply and amplifiers.
- Dr.S.Lakshmi suggested to introduce 'Antenna for 5G application and Software Tools for Antenna Design and Analysis' in the course Antenna and Wave Propagation. Dr.N.Sivakumaran, Prof.,NIT, Trichy accepted the inclusion and also suggested to introduce simulation through HFSS in the class.
- Dr.M.Sumathi suggested to include the topics Li-Fi in the course Optical Communications. Dr.N.Shivakumaran accepted and suggested to remove WDM, SONET/SDH, ATM,IP over WDM.
- Dr.Sugadev presented the syllabus revision for 'Embedded Processors'.Board accepted the change and Mr.J.Visweswaran suggested to include System on Chip concept in the syllabus.
- Syllabus revision proposed in 'High Performance Computing' by Dr.Dr.S.Poorna Pushpa Kala was accepted by Dr.M.D.Selvaraj.
- Dr.S.Barani suggested to remove 'Basics of Spark-Programming in Scala'for the course 'Real Time Data Analytics'. Mr.J.Visweswaran accepted the changes and

suggested to include function programming in Scala. Dr.N.Sivakumaran suggested to include Case studies on influx and Grafana.

- Having discussed the revisions in the existing courses, faculty then presented the syllabus for the new courses. Dr.P.Chitra presented the syllabus for Digital Image Processing for Real Time Applications and Deep Learning Neural Networks. Dr.M.D.Selvaraj accepted the syllabus for both the courses.
- Dr.M.Sugadev presented the syllabus for 5G communication. Dr.N.Sivakumaran suggested to include mmWave communication in the syllabus.
- Dr,S.Poornapushpakala presented the syllabus for Machine Learning using Python. Mr.Visweswaran suggested that Least Squares Optimization, Collaborative Filtering and related topics can be included.
- Dr.S.Barani presented the syllabus for IoT for Real time Applications. Dr.N.Sivakumaran accepted the syllabus and he suggested that students can be made to do miniprojects in this course.
- Dr.M.Sugadev presented the syllabus for Drone Electronics. Syllabus was accepted by the board and Dr.M.D.Selvaraj suggested that this course can be offered to all branches of Engineering.
- Dr.P.Kavipriya presented the syllabus for Industrial Internet of Things. Mr.Visweswaran suggested that Middleware Software protocol can be included.
- Dr.S.lakshmi presented the syllabus for eHealth and Dr.N.Sivakumaran suggested that mobile application development for biomedical applications can be included as part of the syllabus.
- Dr.N.M.Nandhitha informed the Board that part of the syllabus in SEC1320 Embedded Systems will be delivered by the industry expert so as to make the students understand the applications of embedded systems in industries. Mr.J.Visweswaran appreciated the initiative and suggested that at least one course in each semester can be identified for partial delivery of syllabus by expert from industry.

Table 1. Revision Carried out in the courses

SL NO	COURSE CODE	COURSE NAME	DELETED TOPICS	ADDED TOPICS
1	SECA1307	ELECTRONIC CIRCUITS I	UNIT 5 Linear mode power supply - Rectifiers - Half-Wave Rectifier - Full-Wave Rectifier - Filters-L,C, LC, CLC Filter-Regulators -Zener Diode regulator- Linear series, shunt voltage Regulators - Switched mode power supply (SMPS) - Large Signal Amplifiers - Class A, Class B, Class C, Class D- Distortion	UNIT 5 Current sources for biasing - Current steering circuits - Current mirror with improved performance (Cascode mirror, Wilson, Widlar). Large and small signal operation of Differential pair circuit Differential pair with active load

			in power amplifiers	
2	SECA1505	ANTENNA AND WAVE PROGRAGATION	UNIT 5 Anechoic Chamber-Radiation Pattern Measurement-Gain measurement-Beamwidth and Directivity Measurement-Impedance Measurement - Measurement of radiation efficiency- Ionospheric measurements - Vertical incidence measurements of the ionosphere - Relation between oblique and vertical incidence transmission - System Issues - antenna noise.	UNIT 5 Concepts and Benefits of Smart antennas - Fixed weight beamforming- Adaptive beamforming - Design of Planar array antennas for Beamforming applications - Feed techniques for Planar arrays - Role of Smart Antennas in Green Communications and 5G wireless communications - Software Tools for Antenna Design and Analysis
3	SEC1407	OPTICAL COMMUNICATIONS	UNIT 5 Applications- Military	UNIT 3 Vertical cavity surface emitting laser, Resonant cavity enhancement UNIT 5 Optical OFDM, High-speed Light-Waveguides, Reconfigurable optical add/drop multiplexer, Light-Fidelity (Li-Fi) Technology- Introduction, working principle, Comparison of Li-Fi and Wi-Fi, Li-Fi networks, Applications. Case study: Evaluation of building a Fiber Optic network
4	SECA3019	EMBEDDED PROCESSORS	UNIT 5 Introduction - fixed and floating point -Core architecture of ADSP218X- Arithmetic Logic Unit (ALU) - Multiplier and Accumulator (MAC) Unit- Barrel Shifter- Data Address Generator (DAG)- Program Sequencer- Functional Diagram of TMS320C54XX.	UNIT 5 Introduction to ARM CORTEX series, improvement over classical series and advantages for embedded system design. CORTEX A, CORTEX M, CORTEX R processors series, versions, features and applications, need of operating system in developing complex applications in embedded system, Firmware development for ARM Cortex, Survey of CORTEX M3 based controllers, its features and comparison
5	SECA7020	HIGH PERFORMANCE COMPUTING	UNIT 4 Intrusion Detection and Prevention, Intrusion Risks, Security Policy, Monitoring and Reporting of Traffics, Traffic Shaping, Investigating and Verifying Detected Intrusions.	UNIT 4 Scheduling Parallel Jobs on Clusters, Parallel Programming Models, Parallel and High Performance programming

			Reporting and Documenting Intrusions. UNIT- 5 Define the Types of Intrusion Prevention Systems, Intrusion Prevention System Basics, and Limitations of Intrusion Prevention System, Spoof Prevention, Denial of Service (DoS), and Quality of Service (QoS) Policy, Web Application Firewall, Packet Signature and Analysis.	languages, Dependence Analysis of Data arrays UNIT- 5 Quantum computing and its issues
6	SECA7023	REAL TIME DATA ANALYTICS	UNIT 1 Basics of Spark-Programming in Scala	UNIT 1 Expressions-Functions – Classes- File I/O – Exceptions – Combining UNIT-2 Distributed Storage- Parallelism, Regression. Classification and Clustering with Spark UNIT-4 Kafka Architecture and Components, Kafka Cluster, Kafka Producer, Kafka Consumer

- Dr.Lalithakumari.S, suggested few modifications in the course ‘Industrial Unit Operations’. She proposed the inclusion of topics leaching and extraction. It has been accepted by Dr.Sivakumaran, and he suggested to include the same along with mixing and separation unit operations. Dr. D.Marshiana suggested that ‘Humidification, de-humidification’ can be added. Dr. Sivakumaran agreed for the inclusion.
- Dr.Lalithakumari presented the syllabus for an elective course ‘Optimal Control Systems’. Dr.Sivakumaran accepted the syllabus change suggested that students may be asked to do mini projects in this course.

COURSE CODE	COURSE NAME	DELETED TOPICS	INCLUDED TOPICS
SEIA1402	Industrial Unit Operations	--	Unit-4 Humidification, De-humidification Unit-2 Leaching and extraction

- Dr.V.Sivachidambaranathan, Prof.&Head, Dept.ofElectricalandElectronicsEngineerin requested the board to shift the course "Applied Thermodynamics" to elective. Dr.M.D.Selvaraj accepted the suggestions.
- Dr.A.Ramesh babu and Dr.S.D.Sundar Singh Jebaseelanputforth the syllabus of the new courses, 'Modern Power Converters'and'Distributed Generation and Microgridss'for the approval of the board. Dr N Sivakumaran approved the Syllabus for these new courses.
- BoS members are happy that the new and the revised courses enhance employability/ Entrepreneurship/Skills of the students.

EXTERNAL MEMBERS:

1. Dr.N.Sivakumaran
2. Dr.M.D.Selvaraj
3. Mr.J.Visweswaran

INTERNAL MEMBERS:

1. Dr.N.M.Nandhitha *nm*
2. Dr.T.Ravi *Dr*
3. Dr.P.Chitra *Chitra*
4. Dr.S.Barani *Barani*
5. Dr.S.Poornapushpakala. *Poornapushpakala*
6. Dr.M.Sumathi *Sumathi*
7. Dr.S.Lakshmi *Lakshmi*
8. Dr.P.Kavipriya *P*
9. Dr M Sugadev *msug*
10. Dr .E.Anna Devi *E. Anna*
11. Ms.S.Yogalakshmi *yoga*
12. Dr.LalithaKumari.S *Lalitha*
13. Dr.Pandian.R *Pandian*
14. Dr.Marshiana.D *Marshiana*
15. Dr.V.Sivachidambaranathan *Sivachidambaranathan*
16. Dr.D.Susitra *Susitra*
17. Dr.R.Vanitha *Vanitha*
18. Mrs.D.Ramya *D. Ramya*
19. Mrs.P.Sivagami *P. Sivagami*

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
SYLLABUS REVISION

SL.NO	COURSE CODE	COURSENAME
1	SECA1307	ELECTRONIC CIRCUITS I
2	SECA1505	ANTENNA AND WAVEPROGRAGATION
3	SEC1407	OPTICALCOMMUNICATIONS
4	SECA3019	EMBEDDEDPROCESSORS

SECA1302	ELECTRONIC CIRCUITS - I	L	T	P	Credits	TotaMarks
		3	*	0	3	1

PAPER PATTERN

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Signal

UNIT 5 POWER SUPPLIES AND POWER AMPLIFIERS 9 Hrs.

Linear mode power supply - Rectifiers - Half-Wave Rectifier - Full-Wave Rectifier - Filters-L,C, LC, CLC Filter- Regulators -Zener Diode regulator- Linear series, shunt voltage Regulators - Switched mode power supply (SMPS) – Large Signa Amplifiers – Class A, Class B, Class C, Class D- Distortion in power amplifiers.

Max. 45 Hrs.

COURSE OUTCOMES

On completion of the course, student will be able to

CO1 - Acquire knowledge of simple BJT circuits design and implement circuits with transistor biasing design.

CO2 - Draw the equivalent circuits of BJT and FET.

CO3 - Understand the working principles, Frequency response characteristics of BJT and FET.

CO4 - Compare the frequency response characteristics of BJT and FET amplifiers.

CO5 - Design and troubleshoot simple power supplies and Analyse the performance parameters of power supplies.

CO6 - Understand and identify the performance level in power amplifiers and checking its distortion levels.

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Max. Marks:

20 Marks

80 Marks

PART A: 10 Questions of 2marks each-No choice

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PART B: 2 Questions from each unit of internal choice; each carrying 16 marks

Color red indicates the deleted portion. Color yellow indicates the added portion

SECA1307 (Revised)	ELECTRONIC CIRCUITS-I			L	T	P	Credits	Total marks
					3	0	0	3
Prerequisite: Electron Devices				CoRequisite: Electronic Circuits Lab				
Course Objectives <ul style="list-style-type: none"> • To understand the method of biasing transistors. • To acquire the knowledge of equivalent circuits. • To understand the frequency response of amplifiers. • To provide foundation and confidence to the student to troubleshoot and fault analysis of power supplies and power amplifiers. • To develop current mirrors and differential operations. 								
UNIT	CONTENTS							HOURS
I	Biasing of BJT and FET BJT– Need for biasing – Various biasing methods of BJT- Bias Circuit Design- DC Load Line – DC analysis of Transistor circuits- AC Load Line- AC analysis of Transistor Circuits- Quiescent Point – Thermal stability - Stability factors - Biasing of JFET- Various biasing methods of JFET- JFET Bias Circuit Design- MOSFET Biasing- Two port network.							9
II	EQUIVALENT MODELS OF BJT AND FET AMPLIFIERS Hybrid model- Analysis of CE, CC and CB amplifiers using Hybrid equivalent circuit to obtain gain, input impedance and output impedance--Small Signal Amplifiers –Analysis of CE, CC and CB amplifiers using small signal equivalent circuits to obtain gain, input impedance and output impedance. Small Signal equivalent circuit of FET and MOSFET - Analysis of CS, CD and CG JFET amplifiers using small signal equivalent circuits- Analysis of CS, CD and CG MOSFET amplifiers using small signal equivalent circuits							9
III	MULTISTAGE AMPLIFIERS AND FREQUENCY RESPONSE OF BJT AND FET AMPLIFIERS Multistage Amplifiers- Methods of Coupling- RC Coupled- Transformer Coupled –Direct Coupled Amplifiers- Amplifier frequency response – Miller effect- Frequency response of transistor amplifiers with circuit capacitors – BJT frequency response – Low and High frequency analysis of CE, CB, CC-							9

Color red indicates the deleted portion. Color yellow indicates the added portion

	FrequencyresponseofFET -Low andHigh frequencyanalysisofCS,CG,CDJFET&MOSFET.	
IV	POWERSUPPLIESANDPOWERAMPLIFIERS Linear mode power supply - Rectifiers - Half-Wave Rectifier - Full-Wave Rectifier -Filters-L, C, LC, CLC Filter- Regulators - Zener Diode regulator- Linear series, shuntvoltageRegulators-Switched modepowersupply(SMPS) –Large SignalAmplifiers –ClassA,ClassB,ClassC,ClassD-Distortioninpoweramplifiers.	9

V	CURRENTMIRRORSANDDIFFERENTIALAMPLIFIERS Current sources for biasing – Current steering circuits – Current mirror with improved performance (Cascode mirror, Wilson, Widlar). Large and small signal operation of Differential pair circuit Differential pair with active load - Frequency response of the Differential amplifier	9
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MaximumHours:45**CourseOutcomes**

On completion of the course, the students should be able to

CO1–Design biasing circuits for BJT and FET.

CO2–Analyze BJT and FET amplifiers using equivalent circuit models.

CO3–

Evaluate frequency response of BJT and FET amplifiers in different configurations.

CO4–Design multistage amplifiers using BJT

CO5–

Analyze the performance of power supplies and power amplifiers.

CO6–Design current mirrors and differential amplifiers

TEXT/REFERENCEBOOKS

1. Millman J and Halkias C., "Integrated Electronics", TMH, 2nd Edition, 2017.
2. S. Salivahanan, N. Suresh Kumar and A. Vallavaraj, "Electronic Devices and Circuits", TMH, 2nd Edition, 2017.
3. Adel S. Sedra and Kenneth C. Smith, "Microelectronic Circuits", Oxford University Press, Sixth Edition, 2009.
4. Behzad Razavi, "Fundamentals of Microelectronics", 1st edition, Wiley publication, 2008.
5. Donald A. Neamen, "Electronic Circuits Analysis and Design", McGraw Hill Education (India) Private Ltd., 3rd Edition, 2010.
6. Robert L. Boylestad and Louis Nasheresky, "Electronic Devices and Circuit Theory", Pearson Education, 11th Edition, 2013.
7. Floyd, "Electronic Devices", Pearson Education, 9th Edition, 2012.

SECA1504	ANTENNAS AND WAVE PROPAGATION	L	T	P	Credits	TotalMarks
		3	*	0	3	1

COURSE OBJECTIVES

- To learn the basic antenna parameters.
- To understand the radiation mechanism from the dipole antennas.
- To explore the various antenna arrays and calculate the maxima, minima and half power directions.
- To discuss the characteristics of travelling wave radiators and high frequency antennas.
- To study antenna measurements techniques.

UNIT 1 ANTENNA FUNDAMENTALS AND WIRE ANTENNAS 9 Hrs.

Basic Antenna elements-properties of antenna-Isotropic radiator- Antenna parameters(definition only): Radiation Intensity,Radiation pattern, Gain and Directivity, FBR, Effective Length, Effective Aperture, Radiation Resistance, Antenna Terminal(self and mutual) Impedance, Polarization, Beam width, Bandwidth, Antenna temperature, Friis transmission formula. WireAntennas: Retarded Potentials, Short Electric Dipole, Radiation from an alternating current element, Half wave Dipole,quarter wave monopole - Fields, Power radiated and Radiation Resistance.

UNIT 2 ANTENNA ARRAYS 9 Hrs.

Types of arrays - Broad side, End fire, Collinear, Parasitic arrays.-Arrays of two point sources. N element of uniform lineararrays –Array of N elements with equal spacing and currents equal in magnitude and spacing (broad side array)- Array of Nelements with equal spacing and currents equal in magnitude but with progressive phase shift(end fire array)-Hansenwoodyard array -Pattern Multiplication - Binomial arrays.

UNIT 3 TRAVELLING WAVE AND BROADBAND ANTENNAS 9 Hrs.

Effect of ground on ungrounded antenna-Methods of excitation-Travelling wave radiators: basic concepts, Long wireantennas-field strength calculations and patterns- V-antennas, Rhombic Antennas, Small Loop antennas- Concept of shortmagnetic dipole, Helical Antennas, Folded Dipole Yagi-Uda Arrays, Log periodic antennas. Reflector Antennas: Huygens'principle-Flat Sheet and Corner Reflectors, Paraboloidal Reflectors, Cassegrain Feeds. Slot antennas-Babinets principle,Horn antennas, Lens antennas, Microstrip antennas.

UNIT 4 PROPOGATION 9 Hrs.

Factors involved in the propagation of Radio Waves- Ground wave propogation, Structure of Ionosphere and its effects onradio waves- Refraction and Reflection of sky wave by the ionosphere, Ray paths-Measures of Ionosphere propagation:critical frequency, skip distance, virtual height, Maximum usable frequency, Fading of signals: Selective fading – Diversityreception, Space wave propagation-Considerations in space wave propagation, Atmospheric effects in space wavepropagation, Super Refraction - Duct Wave Propagation.

UNIT 5 ANTENNA MEASUREMENT 9 Hrs.

Anechoic Chamber-Radiation Pattern Measurement-Gain measurement-Beamwidth and Directivity Measurement-Impedance Measurement - Measurement of radiation efficiency- Ionospheric measurements - Vertical incidence measurements of the ionosphere - Relation between oblique and

Max. 45 Hrs.

COURSE OUTCOMES

On completion of the course, student will be able to

CO1 - Define the antenna basic parameters.

CO2 - Explain the radiation mechanism from the dipole elements.

CO3 - Classify antenna arrays and infer their significance.

CO4 - Analyze the characteristics of low and high frequency antennas.

CO5 - Compare the properties and measures of various radio wave propagation.

CO6 - Perform antenna measurements and analyze the parameters and develop the basic skills for designing practicalantennas.

TEXT / REFERENCE BOOK

1. K.D.Prasad, "Antennas and Wave Propagation", 3rd Edition, Satya Prakasan, New Delhi,2003.
2. R.L. Yadava, "Antennas and Wave Propagation", 2nd Edition, PHI Learning Private Limited, New Delhi,2011.
3. Balanis, C.A., "Antenna Theory and Design", 4th Edition, John Wiley and Sons, 2016.
4. Jordan, E.C. and Balmain, K.G., "Electromagnetic Waves and Radiating Systems", 2nd Edition, Prentice Hall of India,2006.
5. Collin, R.E., "Antennas and Radio Wave Propagation", McGraw Hill, 1985.

END SEMESTER EXAMINATION QUESTION PAPER PATTERN

Max. Marks: 100 Exam Duration: 3 Hrs.

PART A: 10 Questions of 2 marks each – No choice 20 Marks

PART B: 2 Questions from each unit of internal choice; each carrying 16 marks 80 Marks

Sathvabama Institute of Science and Techn

Department of Electronics and Communication Engi

SECA1505 (revised)	ANTENNAANDWAVEPROPAGATION	L	T	P	Credits	Total marks
		2	0	2	3	100
Prerequisite: ElectromagneticTheory		CoRequisite: NIL				
CourseObjectives						
<ul style="list-style-type: none"> ☐ Tounderstandtheradiationmechanismofdifferentantennas,theirparametersanddesign aspects. ☐ Tolearnaboutantennaarraysandtheirradiationpattern. ☐ Tolearnabouttravellingwaveandbroadbandantennas. ☐ Tounderstandelectromagneticwavepropagationovergroundandthroughdifferentlaye rsofatmosphere. ☐ TolearnaboutSmartAntennas 						
UNIT	CONTENTS					HOURS
I	ANTENNA FUNDAMENTALSAND WIREANTENNAS Antenna Radiation Mechanism - Isotropic radiator- Antenna parameters : RadiationIntensity, Radiation pattern, Gain and Directivity, FBR, Effective Length, RadiationResistance, Antenna Impedance, Polarization, Beam width, Bandwidth, Antennatemperature- Friis transmission formula. Retarded Potential - Wire Antennas: Short ElectricDipole,Radiationfromanalternatingcurrentelement,Half-waveDipole, quarterwavemonopole-Fields,PowerradiatedandRadiationResistance.					9
II	ANTENNAARRAYS Types of arrays - Broad side, End fire, Collinear, Parasitic arrays. - Arrays of two pointsources - N-element of uniform linear arrays – Array of N elements with equal spacingand currents equal in magnitude and spacing (broad side array)- Array of N elementswithequalspacingandcurrentsequal inmagnitudebut withprogressivephaseshift (endfirearray)-Hansen-Woodyardarray-Pattern Multiplication-Binomialarrays.					9
III	TRAVELLINGWAVEANDBROADBANDANTENNAS Travelling wave radiators: basic concepts, Long wire antennas - field strengthcalculations and patterns - V-antennas, Rhombic Antennas, Small Loop antennas-Concept of short magnetic dipole, Helical Antennas, Folded Dipole Yagi-Uda Arrays,Log periodic antennas. Reflector Antennas: Huygens' principle - Flat Sheet and CornerReflectors,ParaboloidReflectors,CassegrainFeeds.Slotantennas-Babinet's					9

	principle,Hornantennas,Lensantennas,Microstripantennas.	
IV	<p>WAVEPROPOGATION</p> <p>Factors involved in the propagation of Radio Waves - Ground wave propagation,Structureoflonosphereanditseffectsonradiowaves- RefractionandReflectionofskywave by the ionosphere, Ray paths- Measures of lonosphere propagation: criticalfrequency, skip distance, virtual height, Maximum usable frequency, Fading of signals:Selectivefading-Diversityreception,Spacewavepropagation- Considerationsin</p>	9

	spacewavepropagation,Atmosphericeffectsinspacewavepropagation,Super Refraction-DuctWavePropagation.	
V	SMARTANTENNAS Concepts and Benefits of Smart antennas - Fixed weight beamforming- Adaptivebeamforming – Design of Planar array antennas for Beamforming applications – FeedtechniquesforPlanararrays- RoleofSmartAntennasinGreenCommunicationsand 5Gwirelesscommunications– SoftwareToolsforAntennaDesignandAnalysis	9

MaximumHours:45**CourseOutcomes**

Oncompletionofthecourse,studentshouldbeableto

CO1-

Designlinearwireantennasforgivenspecifications.

CO2-Designlinearandnon-

linearantennaarraysforgivenbeamwidthandgain.CO3-

Analyzetheperformanceof ApertureandReflectorAntennas

CO4-Designmicrostripantennasforgivenspecifications.

CO5-

AnalysetheproblemsassociatedwithradiowavepropagationintheatmosphereC

O6-Designplanarantennaarraysforbeamformingapplications.

TEXT/REFERENCEBOOKS

1. JohnD.Kraus,RonaldJMarhefkaandAhmadS.Khan,“AntennasandWavePropagation”,5th
Edition,McGrawHill,2017.
2. Balanis,C.A.,“AntennaTheoryandDesign”,4thEdition,JohnWileyandSons,2016.
3. MohammadAbdulMatin,“Wideband,MultibandandSmartAntennaSystems,1stEdition,
Springer,2021.
4. R.L. Yadava, “Antennas and Wave Propagation”, 2ndEdition, PHI Learning
Private Limited, NewDelhi,2011.
5. Jordan,E.CandBalmainK.G,“ElectromagneticWavesandRadiatingSystems”,2ndEdi
tion,PrenticeHallofIndia,2006.
6. K.D.PrasadandSatyaPrakasan,“AntennasandWavePropagation”,3rdEdition,TechI
ndia,NewDelhi,2003.

Color red indicates the deleted portion. Color yellow indicates the added portion

SEC1403	OPTICAL COMMUNICATIONS (For ECE and ETCE)	L	T	P	Credits	Total Marks
		3	0	0	3	100

COURSE OBJECTIVES

To

introduce the principle of light propagation through optical fibers. To understand signal distortion mechanisms in the fiber.

To introduce optical transmitters and receivers for fiber/freespace links and the fiber optic couplers, connectors involved.

To introduce optical network concepts and components involved and its Applications.

UNIT 1 INTRODUCTION TO OPTICAL FIBERS 9Hrs.

Basics of optical communication system, light propagation in optical fibers, Optical spectral bands, Advantages of optical fiber communication over other communication systems, Ray theory and mode theory. Total internal reflection, Acceptance angle, Numerical aperture, phase and group velocity, cutoff wavelength & group delay. Different types of optical fibers, refractive index profiles & mode transmission.

UNIT 2 TRANSMISSION CHARACTERISTICS AND OPTICAL AMPLIFIERS 9Hrs.

Characteristics of optical fibers: Attenuation due to absorption, scattering & bending, core and cladding losses, Signal Distortion in optical fibers: Intra modal Dispersion: Material & Waveguide dispersion; Intermodal dispersion: MMSI, MMGI & modal noise.

Optical Amplifiers: Basic concepts, Erbium-Doped Fiber Amplifier, Raman amplifier - principles of operation, amplifier noise, signal to noise ratio, gain, gain bandwidth, intermodulation effects and wavelength range of operation.

UNIT 3 OPTICAL TRANSMITTERS AND RECEIVERS 9Hrs.

Fiber optic Transmitter module. Optical sources - LEDs, LASER diodes - Principles of operation: concepts of linewidth, phase noise, Optical detectors - P-I-N, Avalanche photodiodes : Principles of operation: concepts of responsivity, sensitivity and quantum efficiency, noise in detection. Fiber optic Receiver module.

UNIT 4 COUPLERS, CONNECTORS AND OPTICAL LINK 9Hrs.

Couplers: 2x2 coupler, Tap coupler, star coupler, Connectors: Cylindrical ferrule, Biconical Ferrule, Double eccentric, Splices: Fusion splices, Mechanical splices, Multiple splices. Design considerations in optical links, Point to point Links: Link Power budget, Rise Time budget, Analog Links: CNR, Multichannel transmission techniques - Multichannel Frequency Modulation, Subcarrier multiplexing.

UNIT 5 OPTICAL NETWORKING PRINCIPLES AND APPLICATIONS. 9Hrs.

FDDI, WDM, SONET/SDH, ATM, IP over WDM, Optical LAN Standards - IEEE 802.3, Applications - Military

Max. 45 Hours**TEXT/REFERENCE BOOKS**

1. Gerd Kaiser, "Optical Fiber Communications", 4th edition, Sixth reprint, Tata McGraw Hill, New Delhi, 2009.
2. John M. Senior, "Optical Fiber Communications - Principles and Practice", Third Edition, Pearson Education, 2010.
3. Gerd Keiser, "Optical Communications Essentials", Special Indian Edition, Tata McGraw Hill, New Delhi, 2008.
4. Govind P. Agrawal, "Fiber-Optic Communication Systems", Third Edition, John Wiley & Sons, 2004.
5. Rajiv Ramasamy & Kumar N. Sivarajan, "Optical Networks - A Practical Perspective", 2Ed, Morgan Kaufman 2002.

END SEMESTER EXAM QUESTION PAPER PATTERN**Max. Marks: 100****Exam Duration: 3Hrs.****PART A:** 10 Questions of 2 marks each - No choice**20 Marks****PART B:** 2 Questions from each unit of internal choice, each carrying 16 marks**80 Marks**

SEC1407 (Revised)	OPTICAL COMMUNICATIONS	L	T	P	Credits	Total marks
		3	0	0		
Pre requisite: NIL			Co Requisite: NIL			
Course Objectives <ul style="list-style-type: none"> To introduce the principles of light propagation through optical fibers. To understand signal distortion mechanisms in the fiber. To introduce optical transmitters and receivers for fiber /free space links and the fiber optic couplers, connectors involved. To introduce optical network concepts and components involved and its applications. 						
UNIT	CONTENTS					HOURS
I	INTRODUCTION TO OPTICAL FIBERS Basics of optical communication system, light propagation in optical fibers, Optical spectral bands, Advantages of optical fiber communication over other communication systems, Ray theory and mode theory. Total internal reflection, Acceptance angle, Numerical aperture, phase and group velocity, cutoff wavelength & group delay. Different types of optical fibers, refractive index profiles & mode transmission.					9
II	TRANSMISSION CHARACTERISTICS AND OPTICAL AMPLIFIERS Characteristics of optical fibers: Attenuation due to absorption, scattering & bending, core and cladding losses, Signal Distortion in optical fibers: Intra modal Dispersion: Material & Waveguide dispersion; Intermodal dispersion: MMSI, MMGI & modal noise. Optical Amplifiers: Basic concepts, Erbium-Doped Fiber Amplifier, Raman amplifier -principles of operation, amplifier noise, signal to noise ratio, gain, gain bandwidth, intermodulation effects and wavelength range of operation.					9
III	OPTICAL TRANSMITTERS AND RECEIVERS Fiber optic Transmitter module. Optical sources- LEDs, LASER diodes- Principles of operation: concepts of line width, phase noise, Vertical cavity surface emitting laser. Optical detectors-P-I-N, Avalanche photodiodes, Resonant cavity enhancement, Photodetector : Principles of operation: concepts of responsivity, sensitivity and quantum efficiency, noise in detection. Fiber optic Receiver module.					9
IV	COUPLERS, CONNECTORS AND OPTICAL LINK Couplers: 2x2 coupler, Tap coupler, star coupler, Connectors: Cylindrical ferrule, Biconical Ferrule, Double eccentric, Splices: Fusion splices, Mechanical splices, Multiple splices. Design considerations in optical links, Point to point Links: Link Power budget, Rise Time budget, Analog Links: CNR, Multichannel transmission techniques-Multichannel Frequency Modulation, Subcarrier multiplexing.					9
V	OPTICAL NETWORKS, COMPONENTS AND APPLICATIONS Optical LAN Standards-IEEE802.3, Optical OFDM, High-speed Light-Waveguides, Reconfigurable optical add/drop multiplexer, Light-Fidelity (Li-Fi)					9

Color red indicates the deleted portion. Color yellow indicates the added portion

Maximum Hours: 45

Course Outcomes

On completion of the course, the student should be able to

CO1-Apply the mathematical concepts to compare different types of optical fibers, modes and configuration

CO2-Analyze the transmission characteristics of optical fibers

CO3-Examine the optical sources and detectors for use in optical communication system

CO4-Construct launching and coupling of optical fibers

CO5-Design high speed optical communication networks

CO6-Design wireless communication system using Li-Fi

TEXT / REFERENCE BOOKS

1. Gerd Kaiser, "Optical Fiber Communications", 4th edition, Sixth reprint, Tata Mc Graw Hill, New Delhi, 2009.
2. John M. Senior, "Optical Fiber Communications- Principles And Practice", Third Edition, Pearson Education, 2010.
3. Govind P. Agrawal, "Fiber-Optic Communication Systems", Third Edition, John Wiley & Sons, 2004.
4. Rajiv Ramasamy & Kumar N. Sivarajan, "Optical Networks - A Practical Perspective", 2 Ed, Morgan Kaufman 2002.
5. Svilen Dimitrov & Harald Haas, "Principles of LED Light Communications: Towards Networked Li-Fi, Cambridge University Press, 2015

SECA3003	EMBEDDED PROCESSORS	L	T	P	Credits	Total Marks
		3	0	0	3	100

COURSE OBJECTIVES

- To understand the architecture of PIC, ARM and DSP processor.
- To familiarise the students in writing assembly programming and interfacing with Peripherals using PIC Microcontroller.
- To understand the difference between Microcontroller and DSP processor.

UNIT 1 INTRODUCTION TO PIC MICROCONTROLLERS

9Hrs.

Introduction to PIC – Compare Von Neumann architecture and Harvard architecture, RISC Vs CISC - Features of PIC - Architecture of PIC16F8XX- Pin Configuration- Addressing modes- Instruction set- Simple Programs.

UNIT 2 PIC ON CHIP PERIPHERALS

9Hrs.

Memory- Core SFR- Interrupts- I/O Ports- Timers- CCP modules- Capture Mode- Compare Mode- PWM Mode- Serial communication module- USART- SPI interface- I2C interface Analog Comparator, ADC.

UNIT 3 APPLICATIONS USING PIC

9Hrs.

Programming Languages- Compilers- Assemblers- Directives- MPLAB IDE- Interfacing- Switches & Push buttons- LEDs- seven segment displays- LCD- Keypad Interfacing, Timer applications- Case study Temperature monitoring System- Case study Digital Clock.

UNIT 4 ARM PROCESSOR

9Hrs.

Architecture of ARM Controller- Registers, Pipeline organization 3 stage & 5 stage, Thumb mode of operation- D/A and A/D converter, sensors, actuators and their interfacing – Case study- Temperature sensing, Light sensing, Introduction to Internet of Things, smart home concepts.

UNIT 5 DSP PROCESSORS

9Hrs.

Introduction - fixed and floating point – Core architecture of ADSP218X- Arithmetic Logic Unit (ALU) – Multiplier and Accumulator (MAC) Unit- Barrel Shifter- Data Address Generator (DAG)- Program Sequencer- Functional Diagram of TMS320C54XX.

Max: 45Hrs.

COURSE OUTCOME

On completion of the course, student will be able to

CO1- Understanding architecture and function of PIC microcontrollers.

CO2- Explain the discussion of on-chip peripherals of PIC microcontrollers.

CO3- Do assembly and C program exercises with various I/O modules.

CO4- Understand the architecture of ARM.

CO5-

Understand the need of DSP processor for signal processing applications and its architecture. CO

6- Design real-time application using PIC Microcontroller.

TEXT/REFERENCE BOOKS

1. Danny Causey, Rolin McKinlay, Muhammad Ali Mazidi, "PIC Microcontroller and Embedded Systems: Using assembly and C for PIC18", 1st Edition, Micro Digital Edition; 2nd Edition, 2016.
2. Andrew Sloss, Dominic Symes, Chris Wright, "ARM System Developer's Guide: Designing and Optimizing System Software", Elsevier, 1st Edition, 2004.
3. F. Vahid and T. Givargis, "Embedded System Design: A Unified Hardware/Software Introduction", Wiley India Pvt. Ltd., 2002.
4. B Venkataramani MBhaskar, "Digital Signal Processors Architecture, Programming and Applications", Tata McGraw Hill, 2nd Edition, 2002.

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USEFULLINKS

1. <https://www.mikroe.com/ebooks/pic-microcontrollers-programming-in-c>
2. www.analog.com.
3. www.ti.com.
4. www.dspguide.com.
5. <https://swayam.gov.in/courses/5424-jan-2019-embedded-system-design-with-arm>

ENDSEMESTEREXAMINATIONQUESTIONPAPERPATTERN

SECA3019	EMBEDDED PROCESSORS	L	T	P	Credits	Total marks
		3	0	0		
Pre requisite: Nil			Co Requisite: Nil			
Course Objectives <ul style="list-style-type: none"> ● To analyze the features of various embedded processors ● To analyze the On-chip peripherals ● To develop ARM processor-based applications ● To design innovative applications by interfacing the processors with real world ● To analyze various ARM cortex processors ● To develop firmware for ARM cortex processors 						
UNIT	CONTENTS					HOURS
I	INTRODUCTION TO EMBEDDED PROCESSORS Introduction to embedded processors– Compare Von Neumann architecture and Harvard architecture, RISC Vs CISC – System on Chip (SoC)-Introduction to SoC Architecture, An approach for SOC Design, System Architecture and Complexity. Processor Selection for SOC, Basic concepts in Processor Architecture, Overview of SOC external memory, Internal Memory, Scratchpads and Cache memory, SOC Memory System, Models of Simple Processor – memory interaction, SOC Standard Buses					9
II	EMBEDDED PROCESSORS ON CHIP PERIPHERALS Memory - Interrupts - I/O Ports-Timers & Real Time Clock (RTC), Watch dog timer - CCP modules - Capture Mode - Compare Mode-PWM Mode - Serial communication module - USART - SPI interface - I2C interface, Analog Comparator, Analog interfacing and data acquisition.					9
III	ARM PROCESSOR Architecture of ARM Controller – Registers, Pipeline organization 3 stage & 5 stage, Thumb mode of operation - D/A and A/D converter, sensors, actuators and their interfacing – Case study- Digital clock, Temperature sensing, Light sensing, Introduction to Internet of Things, smart home concepts					9
IV	REAL WORLD INTERFACING USING ARM PROCESSOR Interfacing the peripherals to LPC2148: GSM and GPS using UART, on-chip ADC using interrupt (VIC), EEPROM using I2C, SD card interface using SPI, on-chip DAC for waveform generation.					9
V	ARM CORTEX PROCESSORS Introduction to ARM CORTEX series, improvement over classical series and advantages for embedded system design. CORTEX A, CORTEX M, CORTEX R processors series, versions, features and applications, need of operating system in developing complex applications in embedded system, Firmware development for ARM Cortex, Survey of CORTEX M3 based controllers, its features and comparison					9

Maximum Hours: 45

Color red indicates the deleted portion. Color yellow indicates the added portion

Course Outcomes

On completion of the course, the student will be able to

CO1-Analyze the architectures of different Embedded Processors

CO2-Identify an appropriate on chip peripherals for serial and parallel communication

CO3-Examine the functions of ARM processors

CO4-Develop real time applications using ARM processors

CO5-Develop a firmware for embedded applications

CO6-Develop innovative products using Embedded processors

TEXT / REFERENCE BOOKS

1. F. Vahid and T. Givargis, "Embedded System Design: A Unified Hardware/Software Introduction", Wiley India Pvt. Ltd., 2002.
2. Michael J. Flynn and Wayne Luk, "Computer System Design System-on-Chip", Wiley India Pvt. Ltd.
3. Steve Furber, "ARM System on Chip Architecture ", 2nd Edition, 2000, Addison Wesley Professional.
4. S. Pasricha and N. Dutt, Morgan Kaufmann, On-Chip Communication Architectures, System on Chip Interconnect, -Elsevier Publishers 2008
5. Mark Fisher, "ARM Cortex M4 Cookbook", Packt Publishing, 2016.
6. Lyla B. Das, "Architecture, Programming and Interfacing of Low-power Processors ARM 7, Cortex-M", Cengage, 1st Edition, 2017.
7. Joseph Yiu, "The Definitive Guide to the ARM Cortex-M3", Newness, 2nd Edition, 2009