



Society for Applied Microwave Electronics Engineering and Research
Autonomous Society of Ministry of Electronics and Information Technology
Government of India

Date: 26/07/2021

Certificate

*This is to certify that the Project Report entitled "**DESIGNING PCB LAYOUT OF AN AMPLIFIER AND ACTIVE BANDPASS FILTER**" submitted by **RESHMI NAIR**, student of **B.E in ELECTRONICS AND COMMUNICATION, SATHYABAMA INSTITUTE OF SCIENCE AND TECHNOLOGY, JEPPIAAR NAGAR, RAJIV GANDHI SALAI, CHENNAI, TAMILNADU** for the internship carried out in the period from **DECEMBER-2020** to **FEBRUARY-2021** is a bonafide record of the project work of her at SAMEER, Mumbai in **IMSD** Division under our supervision(s).*

MRS. POORNIMA SHRIVASTAVA

Name of Guide

MR. ANIL KULKARNI

Name of HoD

Signature of Manpower coordinator(s),

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